Electronic Acknowledgement Receipt FES ID: 1032364 Application Number: 10826713 Confirmation Number: 2185 Title of Invention: Wafer-level assembly method for chip-size devices having flipped chips First Named Inventor: William D. Boyd Customer Number: 23494 Filer: Yingsheng Tung/Jackie McBride Filer Authorized By: Yingsheng Tung Attorney Docket Number: TI-37214 Receipt Date: 27-APR-2006 Filing Date: 16-APR-2004 Time Stamp: 10:50:29 Application Type: Utility International Application Number:

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt. in due course.